

# EAST Search History

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S9	8	S8 not S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 19:00
S10	333621	processor same (controll\$4 or monitor\$4 or supervis\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 19:02
S11	1644	S10 same (selector\$4 with (rout\$4 or switch\$4 or chang\$4 or alternat\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 19:02
S12	2	S11 same S1 same (fault\$4 or error\$4 or problem or malfunction or fail\$4 or defect\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 19:05
S13	620	(714/11).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 19:05
S14	256	(714/12).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 20:25
S15	493	(714/13).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 21:13
S16	561	(714/10).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 21:14

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	7985	serial near2 controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 17:50
S2	3746	single adj point adj2 fail\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 17:51
S3	111023	selector\$4 with (switch\$4 or chang\$4 or alternat\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 18:53
S4	0	S1 same S2 same S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 17:54
S5	7	S1 and S2 and S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 18:30
S6	15	koishi-takahiro\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 18:49
S7	1540	(two or double or redundan\$4 or mirror or master or slave or standby) same S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 18:50
S8	10	S7 same (selector\$4 with (rout\$4 or switch\$4 or chang\$4 or alternat\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/17 19:02

**EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	616	(714/11).ccls.	US-PGPUB; USPAT	OR	ON	2006/06/18 18:47
L2	255	(714/12).ccls.	US-PGPUB; USPAT	OR	ON	2006/06/18 18:47
L3	489	(714/13).ccls.	US-PGPUB; USPAT	OR	ON	2006/06/18 18:47
L4	436	(714/10).ccls.	US-PGPUB; USPAT	OR	ON	2006/06/18 18:48
L5	6525	serial near2 controller	US-PGPUB; USPAT	OR	ON	2006/06/18 18:48
L6	3485	single adj point adj2 fail\$4	US-PGPUB; USPAT	OR	ON	2006/06/18 18:48
L7	55060	selector\$4 with (switch\$4 or chang\$4 or alternat\$4)	US-PGPUB; USPAT	OR	ON	2006/06/18 18:48
L8	7986	serial near2 controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/18 18:49
L9	1340	(two or double or redundan\$4 or mirror or master or slave or standby) same L8	US-PGPUB; USPAT	OR	ON	2006/06/18 18:55
L10	1541	(two or double or redundan\$4 or mirror or master or slave or standby) same L8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/18 18:56
L11	8	L10 same (selector\$4 with (rout\$4 or switch\$4 or chang\$4 or alternat\$4))	US-PGPUB; USPAT	OR	ON	2006/06/18 18:56
L12	248535	processor same (controll\$4 or monitor\$4 or supervis\$4)	US-PGPUB; USPAT	OR	ON	2006/06/18 18:57
L13	1328	12 same (selector\$4 with (rout\$4 or switch\$4 or chang\$4 or alternat\$4))	US-PGPUB; USPAT	OR	ON	2006/06/18 18:58
L14	1	5 same 13 same (fault\$4 or error\$4 or problem or malfunction or fail\$4 or defect\$4)	US-PGPUB; USPAT	OR	ON	2006/06/18 19:00

Sun, 18 Jun 2006, 7:37:46 PM EST

Edit an existing query or  
compose a new query in the  
Search Query Display.

## Search Query Display

Select a search number (#)  
to:

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- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

## Recent Search Queries

		Results
<a href="#">#1</a>	(( service processor<in>metadata ) <and> ( serial controller<in>metadata ) )<and> ( selector<in>metadata )	0
<a href="#">#2</a>	(( serial<in>metadata ) <and> ( controller<in>metadata ) )<and> ( selector and switch<in>metadata )	1
<a href="#">#3</a>	(( serial<in>metadata ) <and> ( controller<in>metadata ) )<and> ( selector and switch<in>metadata )	1
<a href="#">#4</a>	(( information processor<in>metadata ) <and> ( selector<in>metadata ) )<and> ( standby<in>metadata )	0
<a href="#">#5</a>	(( serial controller<in>metadata ) <and> ( standby<in>metadata ) )<and> ( switching or routing or changing<in>metadata )	0

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serial controller and (standby or mirror or backup or back-up c


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## Terms used

serial controller and standby or mirror or backup or back up or redundant or two or double and selector and switch and fault

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**181** [The evolution of the Sperry Univac 1100 series: a history, analysis, and projection](#)

 B. R. Borgerson, M. L. Hanson, P. A. Hartley  
 January 1978 **Communications of the ACM**, Volume 21 Issue 1

**Publisher:** ACM Press

 Full text available: [pdf\(1.89 MB\)](#)

 Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

The 1100 series systems are Sperry Univac's large-scale mainframe computer systems. Beginning with the 1107 in 1962, the 1100 series has progressed through a succession of eight compatible computer models to the latest system, the 1100/80, introduced in 1977. The 1100 series hardware architecture is based on a 36-bit word, one complement structure which obtains one operand from storage and one from a high-speed register, or two operands from high-speed registers. The 1100 Operating System ...

**Keywords:** 1100 computer series, computer architecture, data management systems, end user facilities, executive control software, multiprocessing, multiprogramming, operating system, programming languages

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 Anita K. Jones, Peter Schwarz  
 June 1980 **ACM Computing Surveys (CSUR)**, Volume 12 Issue 2

**Publisher:** ACM Press

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 Özalp Babaoğlu  
 January 1991 **ACM SIGOPS Operating Systems Review**, Volume 25 Issue 1

**Publisher:** ACM Press

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[full citation](#), [index terms](#)
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 Lee Lin, Michael D. Ernst  
 July 2004 **ACM SIGSOFT Software Engineering Notes , Proceedings of the 2004 ACM SIGSOFT international symposium on Software testing and analysis ISSTA '04**, Volume 29 Issue 4